

### **STE70IE120**

# Monolithic Emitter Switched Bipolar Transistor ESBT $^{\otimes}$ 1200 V - 70 A - 0.014 $\Omega$ Power Module

Target data

### **General features**

V <sub>CS(ON)</sub>	I <sub>C</sub>	R <sub>CS(ON)</sub>	
1V	70A	0.014Ω	

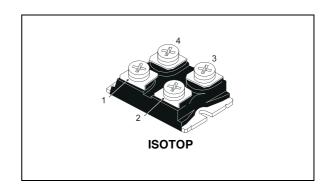
- High voltage / high current Cascode configuration
- Ultra low equivalent on resistance
- Very fast-switch, up to 150 kHz
- Ultra low C<sub>ISS</sub>
- Low dynamic V<sub>CS(ON)</sub>

### **Description**

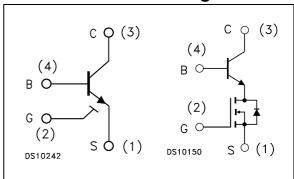
The STE70DE120 is manufactured in monolithic structure, to be used in industrial applications.

### **Applications**

- Solar
- Welding



### Internal schematic diagram



#### **Order codes**

Part number	Marking	Package	Packaging	
STE70IE120	STE70IE120 E70IE120		Tube	

Electrical ratings STE70IE120

# 1 Electrical ratings

Table 1. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V <sub>CS(SS)</sub>	Collector-source voltage (V <sub>BS</sub> = V <sub>GS</sub> = 0V)	1200	٧
V <sub>BS(OS)</sub>	Base-source voltage (I <sub>C</sub> = 0, V <sub>GS</sub> = 0V)	40	V
V <sub>SB(OS)</sub>	Source-base voltage (I <sub>C</sub> = 0, V <sub>GS</sub> = 0V)	12	٧
V <sub>GS</sub>	Gate-source voltage	± 20	V
I <sub>C</sub>	Collector current	70	Α
I <sub>CM</sub>	Collector peak current (t <sub>p</sub> < 5ms)	150	Α
I <sub>B</sub>	Base current	20	Α
I <sub>BM</sub>	Base peak current (t <sub>P</sub> < 1ms)	70	Α
P <sub>tot</sub>	Total dissipation at T <sub>C</sub> = 25°C	TBD	W
T <sub>STG</sub>	Storage temperature	-65 to 150	°C
T <sub>J</sub>	Maximum operating junction temperature	150	°C
V <sub>ISO</sub>	Insulation withstand voltage (AC-RMS) from all four leads to external heatsink	2500	V

### 1.1 Thermal data

Table 2. Thermal data

Symbol	Parameter	Value	Unit
R <sub>thJ-case</sub>	Thermal resistance junction-case (Max) Thermal resistance case-heatsink with conductive grease applied (Max)	TBD	°C/W
R <sub>thc-h</sub>		TBD	°C/W

# 2 Electrical characteristics

**Table 3. Electrical characteristics** 

 $(T_{Case} = 25^{\circ}C$ , unless otherwise specified)

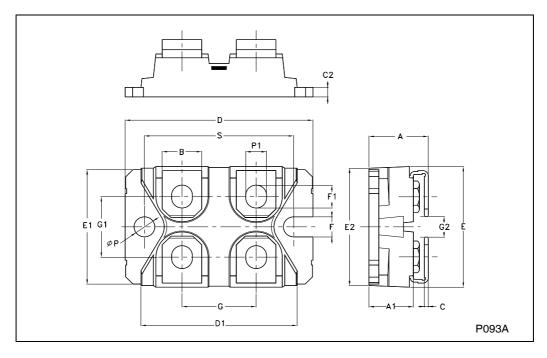
Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Unit
I <sub>CS(SS)</sub>	Collector-source current (V <sub>BS</sub> = V <sub>GS</sub> = 0)	V <sub>CE</sub> = 1200V			100	μА
I <sub>BS(OS)</sub>	Base-source current (I <sub>C</sub> = 0, V <sub>GS</sub> = 0V)	V <sub>BS(OS)</sub> = 40V			10	μА
I <sub>SB(OS)</sub>	Source-base current (I <sub>C</sub> = 0, V <sub>GS</sub> = 0)	V <sub>SB(OS)</sub> = 10V			100	μА
I <sub>GS(OS)</sub>	Gate-source leakage	$V_{GS} = \pm 20V$			500	nA
V <sub>CS(ON)</sub>	Collector-source ON voltage	$V_{GS} = 10V I_C = 70A I_B = 14A$ $V_{GS} = 10V I_C = 45A I_B = 4.5A$		1		V V
h <sub>FE</sub>	DC current gain	$V_{GS} = 10V \ V_{CS} = 1V \ I_C = 70A$ $V_{GS} = 10V \ V_{CS} = 1V \ I_C = 45A$	3 6		7 13	
V <sub>BS(ON)</sub>	Base-emitter ON voltage	$V_{GS} = 10V$ $I_C = 70A$ $I_B = 14A$ $V_{GS} = 10V$ $I_C = 45A$ $I_B = 4.5A$		tbd tbd		V V
V <sub>GS(th)</sub>	Gate threshold voltage	$V_{BS} = V_{GS}$ $I_B = 250 \mu A$	3	3.7	4.5	V
C <sub>iss</sub>	Input Capacitance	$V_{CS} = 25V$ f=1MHz $V_{GS} = V_{CB} = 0$		tbd		pF
Q <sub>GS(tot)</sub>	Gate-source charge	$V_{CS} = 25V$ $V_{GS} = 10$ $V_{CB} = 0$ $I_{C} = 70A$		tbd		nC

# 3 Package Mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect . The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com

### **ISOTOP MECHANICAL DATA**

DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
Α	11.8		12.2	0.465		0.480
A1	8.9		9.1	0.350		0.358
В	7.8		8.2	0.307		0.322
С	0.75		0.85	0.029		0.033
C2	1.95		2.05	0.076		0.080
D	37.8		38.2	1.488		1.503
D1	31.5		31.7	1.240		1.248
Е	25.15		25.5	0.990		1.003
E1	23.85		24.15	0.938		0.950
E2		24.8			0.976	
G	14.9		15.1	0.586		0.594
G1	12.6		12.8	0.496		0.503
G2	3.5		4.3	0.137		1.169
F	4.1		4.3	0.161		0.169
F1	4.6		5	0.181		0.196
Р	4		4.3	0.157		0.169
P1	4		4.4	0.157		0.173
S	30.1		30.3	1.185		1.193



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Revision history STE70IE120

# 4 Revision history

**Table 4. Document revision history** 

Date	Revision	Changes	
04-May-2007	1	Initial EDOCS release	

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